

Attachment to Interview Summary

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The form will be used for the sole purpose of discussion with the Examiner(s), and will and shall not become a part of the official file history.

APPLICANT INITIATED INTERVIEW REQUEST FORM	Applicant	Greene, Michael T.
	Application No.	10/672,186
	Examiner	Daniel Murray (Fax No. 571.270.2773)
	Art Unit	2443
	Status of Application	PUBLISHED
	Docket Number	51249/Z74

Applicant Initiated Interview Request Form					
Tentative Participants:					
(1) Gabriel Fitch (Reg. No. 62905)		(2) Peter Emerson, Michael Greene (Zuken)			
(3) Examiner Murray		(4) SPE			
Proposed Date of Interview: October 15, 2009		Proposed Time: 10:30 AM EST /DCM/ 1:30 PM EST			
Type of Interview Requested: (1) <input checked="" type="checkbox"/> Telephonic (2) <input type="checkbox"/> Personal (3) <input type="checkbox"/> Video Conference					
<u>The call-in information for the interview is:</u>					
Domestic Dial-In #:		888.834.2663			
International Dial In #:		617.786.4658			
Passcode:		47205690			
Exhibit To Be shown or Demonstrated: <input type="checkbox"/> Yes <input checked="" type="checkbox"/> No					
If yes, provide brief description:					
Issues To Be Discussed					
Issues (Rej., Obj., etc)	Claims/Fig. #s	Prior Art	Discussed	Agreed	Not Agreed
(1)					
(2)					
(3)					
(4)					

Brief Description of Arguments to be Presented:

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1. Discuss proposed amendment to Claim 1

1. (Proposed Amendment - Do Not Enter) A computer implemented method of determining the routing of interconnected regions of a routing problem, the interconnected regions being regions of an electrical circuit, comprising: [[by]]

considering all required connections in parallel independently, with each connection being given a chance to be routed while ignoring all other connections,

assembling at least some contextual information about a region and the paths which cross there in the form of at least one conflict object, and

only attempting to resolve crossing conflicts only-when said at least some contextual information about a region and the paths that cross in the region has been assembled.

2. Discuss 112 rejection of Claim 1

3. Discuss 101 rejections of Claims 1-6

4. Discuss distinctions over Andreev and Rostoker references

- a) Rostoker concerns an algorithm for channel routing. In contrast, both Andreev and the present invention concern geometric (free space) routing. Channel routing and geometric routing are fundamentally different and incompatible. Therefore, i) Rostoker cannot be combined with Andreev, ii) Rostoker is not relevant to the present invention.
- b) Andreev does not consider all required connections in parallel independently and simultaneously. This is despite Office action's comments in paragraph a) of page 3 of the outstanding office action for example. Strangely, the Office action appears to acknowledge this by using the word "substantially" at the last line of page 8 and line 9 of page 10 of the office action.
- c) As the Office action mentions, both Rostoker and Andreev concern integrated circuit design. In contrast, the present invention is concerned with PCB design. These fields are separate and distinct, and therefore even if Rostoker and Andreev are combined, the present invention cannot result.

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- d) Looking at the Office action's discussion of Claim 3, Zuken makes the following points:
- i) Firstly, Andreev does not carry out the step (a) of defining for each set of regions to be connected, routing which represents a suitable manner of connecting them, respecting only those crossing conflicts which have been explicitly registered with the set currently being considered.
 - ii) Secondly, Andreev does not carry out the step (c) of collating all such proposed routing and resolving any crossing conflicts in a symmetric manner. Instead, Andreev addresses conflicts serially, i.e. one at a time.

5. Discuss patentability of any of the pending claims based on discussion.

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